

6,800, 905 B2.

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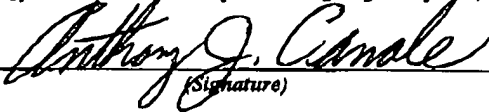
CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			Docket No.
Applicant(s): David M. Fried, Edward J. Nowak, Jed H. Rankin			BUR920010171US1
Application No. 09/683,328	Filing Date Dec. 14, 2001	Examiner Wael Fahmy	Group Art Unit 2814

Invention: IMPLANTED ASYMMETRIC DOPED POLYSILICON FINFET

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**TRANSMITTAL LETTER**  
**(General - Patent Issued)**Docket No.  
**BUR920010171US1**Patentee(s): **David M. Fried, Edward J. Nowak, Jed H. Rankin**

U.S. Patent No.

**6,800,905 B2**

Issue Date

**October 5, 2004**Title: **IMPLANTED ASYMMETRIC DOPED POLYSILICON GATE FINFET****COMMISSIONER FOR PATENTS:**

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,800,905 B2

DATED : October 5, 2004

INVENTOR(S) : David M. Fried, Edward J. Nowak, Jed H. Rankin

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, line 2, "700 Å° C." should read -700°C-; line 6, cancel the text "about 850 Å° to about 1000 Å° C." and insert the following text -about 850°C to about 1000°C-; line 41, "lapper" should read -upper-; line 47, "get" should read -gate-; line 52, "polySiGe" should read -polySiGe-.  
Column 7, line 6, cancel the text "wherein".

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PATENT NO. 6,800,905 B2

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